

About the Workshop

The rapid advancement of Edge AI and reconfigurable computing has significantly increased the demand for FPGA-based system design skills in both academia and industry. This two-day hands-on workshop is designed to provide comprehensive exposure to the Kria FPGA platform, focusing on practical implementation using the Vivado design suite.

The program emphasizes the complete FPGA development flow - from RTL design and synthesis to implementation, bitstream generation, and hardware validation.

The workshop is intended for faculty members, research scholars, and students who wish to strengthen their expertise in FPGA design and explore industry-oriented hardware acceleration techniques.

Objectives of the Workshop

- Understand FPGA architecture and Kria platform fundamentals
- Learn complete hardware design flow using Vivado
- Implement and validate digital systems on FPGA hardware
- Explore concepts of Edge AI acceleration
- Bridge academic learning with industry-oriented applications

Resource Persons



Mr. R. Siva Subramanian
Application Engineer, AMD Xilinx products
WT Microelectronics
Bengaluru



Mr. Gokul Shanmugam
Senior Engineer
HCL Technologies
Chennai



Feature the Future



About the College

K.S.Rangasamy College of Technology (KSRTC), established in 1994 and conferred Autonomous status in 2007, is a premier institution in Tamil Nadu committed to delivering high-quality technical education and fostering innovation-driven learning. The institution offers 13 UG, 10 PG, and 20 Ph.D. programmes, and holds prestigious accreditations including NAAC A++ (score 3.56) and NBA Tier-I for all eligible UG programmes and the MBA programme. KSRTC is recognized under UGC 2(f) & 12(B) and features in national rankings, including the NIRF Engineering Rank Band 201–300 (2024), Innovation Rank Band 51–100 (2023), and QS I-Gauge Diamond Category. The institution has received ₹8.48 crore under the National Technical Textiles Mission (NTTM) and has built a strong research culture with ₹45+ crore in sponsored projects from DST, DBT, DAE, CSIR, DRDO, ISRO, and other national funding agencies. Recognized as a DSIR–SIRO research institute, it encompasses 20 Anna University–approved research centres and a TNSCST IPR Cell, and has secured DBT-STAR and DST-FIST (twice) support. Its vibrant innovation and startup ecosystem is powered by the AICTE IDEA Lab, MSME Business Incubator, and Atal Community Innovation Centre. The Institutions Innovation Council (IIC) has earned the highest star rating and Mentor Institution status, further strengthening the campus innovation culture.



About the Department

Department of Electronics Engineering (VLSI Design and Technology) was established in the year 2023 and offers a four-year undergraduate programme. The curriculum and syllabus are designed in line with industry standards, ensuring a balance between depth of knowledge through specialization and breadth of knowledge through exploration. The programme provides a strong foundation in the core areas of electronics along with specialization in emerging challenges in VLSI engineering. The department is supported by a well-equipped EDA Tools Laboratory with industry-standard tools supported by CDAC-MeitY for VLSI design and simulation, FPGA development tools, PCB design softwares and hardware platforms including Kria, and PYNQ boards to facilitate hands-on learning and hardware prototyping.



K.S.RANGASAMY COLLEGE OF TECHNOLOGY

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Department of Electronics Engineering (VLSI Design and Technology)

Two Day Workshop on Kria Platform for FPGA Design & Edge AI Acceleration

in association with

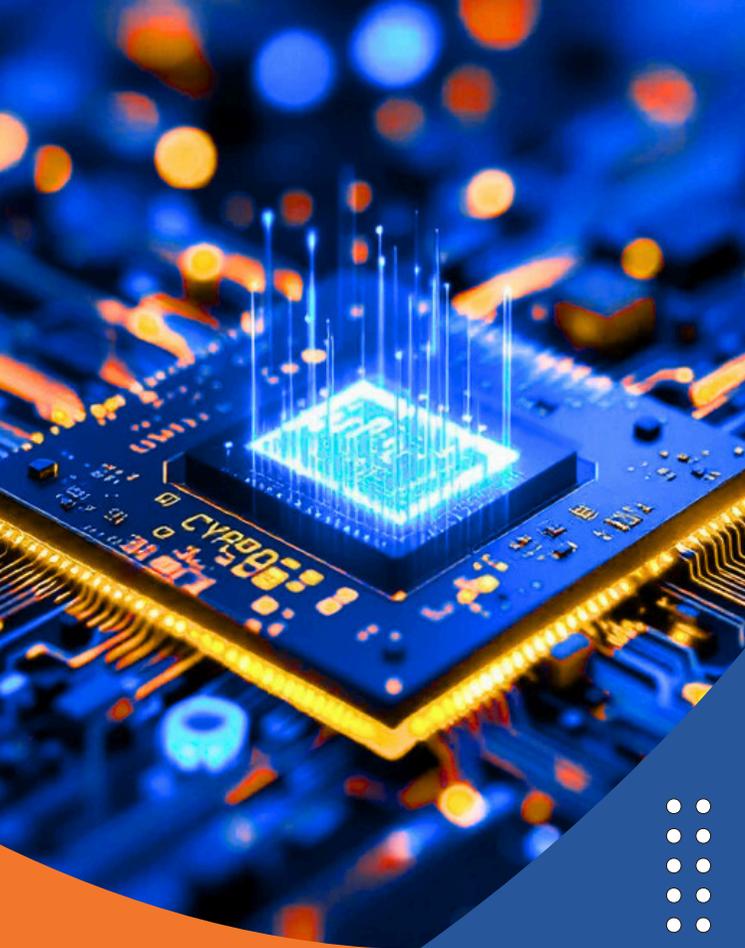


March 16-17, 2026 | Centre of Excellence for EDA Tools

Supported By



www.ksrtc.ac.in



Registration Fees



Research Scholars & Faculty

₹ 1500/-

Students (UG&PG)

₹ 1000/-



Target Audience

Faculty | Researchers | Industry Professionals | Startups | UG/PG Students (VLSI/ECE/EEE/CSE/IT & Allied Branches)



Registration Process



The total number of participants is limited to 30. Selection will be made on a first-come, first-served basis. Applicants are informed to register through the provided Google Form and upload the receipt of the registration-fee payment. The registration fee includes the workshop kit, lunch and refreshments.

Account Details

Account No.: **0751301000264891**
Account Name: **KSRCT Principal Common Account**
IFSC Code: **DBSS0IN0751**
Bank Name: **DBS Bank India Limited**
Branch Name: **Thokkavadi**

Important Dates

Last date for Registration

March 12, 2026

Intimation of Selection

March 13, 2026

Confirmation by Participants

March 14, 2026

Scan to Pay



Boarding & Lodging Accommodation will be available on a payment basis

For more information contact

Faculty Coordinator :
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